Serial No.: 10/810,815

## IN THE SPECIFICATION:

The specification as amended below with replacement paragraphs shows added text with <u>underlining</u> and deleted text with <u>strikethrough</u>.

Please AMEND paragraph [0022], as follows:

Fig. 1 schematically shows the basic construction of a full color surface discharge type plasma display device of the present invention;

Fig. 2 is a perspective view of a full color flat panel ac plasma display device of the present invention;

- Fig. 3A shows a first structure of plasma display devices of the prior art;
- Fig. 3B shows a second structure of plasma display devices of the prior art;
- Fig. 4 shows a third structure of plasma display devices of the prior art;
- Fig. 5 shows a first operation of plasma display devices of the prior art;
- Fig. 6 shows a fourth structure of plasma display devices of the prior art;
- Fig. 7 is one perspective view of another full color flat panel ac plasma display device of the present invention;
- Fig. 8 is a second perspective view of another full color flat panel ac plasma display device of the present invention;
  - Fig. 9 is a first graph illustrating the brightness of display versus the view angle;
  - Fig. 10 is a second graph illustrating the brightness of display versus the view angle;
- Fig. 11 is a first graph to illustrate how the stability of the discharge varies based on the structures of the barriers;
- Fig. 12 is a second graph to illustrate how the stability of the discharge varies based on the structures of the barriers;
- Fig. 13 is a third graph to illustrate how the stability of the discharge varies based on the structures of the barriers;
- Fig. 14 is a block diagram of a full color flat panel ac plasma display device of an embodiment of the present invention;

Serial No.: 10/810,815

Fig. 15 schematically shows the arrangement of the electrodes of the plasma display panel, as in Fig. 14;

Fig. 16 shows the waveform of the addressing voltage of a full color flat panel ac plasma display device in an embodiment of the present invention;

Fig. 17 is a block diagram of a full color flat panel ac plasma display device of another embodiment of the present invention;

Fig. 18 shows the waveform of the addressing voltage of a full color flat panel ac plasma display device in another embodiment of the present invention;

Figs. 19A to 19H show the state of the electric charges at main stages in the operation in accordance with the waveform of the addressing voltage of Fig. 18;

Fig. 20 shows an ideal coverage of a phosphor layer on barriers and a substrate;

Fig. 21 shows the relationship between the thickness of the phosphor layer and the content of phosphor in a phosphor paste;

Figs. 22A to 22C are cross-sectional views, used as an aid for understanding the main steps of forming a phosphor layer in a preferred embodiment of the present invention;

Fig. 23 is a perspective view of a flat panel ac plasma display device;

Figs. 24A and 24B are planar views, used as an aid for understanding the steps of forming address electrodes and barriers on a glass substrate in the prior art; and

Figs. 25A to-25F 25E are planar and segmented views, used as an aid for understanding the steps of forming address electrodes and barriers on a glass substrate in a preferred embodiment of the present invention.